ECE 3040 Microelectronic Circuits

Exam 2

October 28, 2019

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Print your name clearly and largely:

Solution 5

Instructions:

Read all the problems carefully and thoroughly before you begin working. You are allowed to use 1 new sheet of notes (1 page front and back), your note sheet from the previous exam as well as a calculator. **Turn in all note sheets with your exam.** There are 100 total points in this exam+10 additional bonus points. Observe the point value of each problem and allocate your time accordingly. SHOW ALL WORK AND CIRCLE YOUR FINAL ANSWER WITH THE PROPER UNITS INDICATED. Write legibly. If I can not read it, it will be considered to be a wrong answer. Do all work on the paper provided and **DO NOT SEPARATE THE EXAM PAGES**. Turn in all scratch paper, even if it did not lead to an answer. Report any and all ethics violations to the instructor. Good luck!

Sign your name on **ONE** of the two following cases:

I DID NOT observe any ethical violations during this exam:

I observed an ethical violation during this exam:

First 20% True /False and Multiple Choice - Select the most correct answer(s)

- 1.) (2-points) (True) False: Solar Cells, Photodiodes, LEDs, LASER Diodes are all forms of diodes performing different optoelectronic functions.
- 2.) (2-points) (True) False: An ideal diode model acts as a perfect switch and (1997) be used when the turn on voltage is negligible.
- 3.) (2-points) True (Falser Large n and p-type doping concentrations lead to large leakage currents in diodes.
- 4.) (2-points) True False: The Ebers-Moll Model suggests that a BJT biased into saturation can be replaced with two very small current sources.
- 5.) (2-points) True / False? The EBERS-Moll Model can be used for all bias modes of the BJT except Inverse Active.
- 6.) (2-points) True False: For a BJT in forward active mode, the electric field in a reverse biased collector-base junction acts like a carrier extractor that strips the carriers from the base that were injected from the emitter.
- 7.) (2-points) True) False: The amplification process of a forward active BJT results from a small base current controlling a much larger collector emitter current.
- 8.) (2-points) True / False: Zener breakdown results from the Pauli Exclusion principle from Quantum Mechanics.
- 9.) (2-points) True (False:) In a forward biased diode, the current results from the electric field drifting carriers over the energy barrier and thus, drift current dominates over diffusion current.
- 10.) (2-points) True //False: A BJT in saturation is modeled as two series connected batteries.

11) (15 Points)

Draw the Energy band diagram of a Si (Eg=1.1 eV) Zener diode biased under equilibrium conditions and (separately) under 10 Volts reverse bias. Label the p and n sides of the junction, the conduction band and fermi and quasi-fermi levels in the quasi neutral regions. With the help of arrows indicated carrier motion and current flows, empty circles representing holes and solid circles representing electrons, show the avalanche multiplication process resulting a large current flow.



12.) (15 points total in 2 parts)

An LED is specified to have rated optical output at 20 mA and 1.4 volts turn on voltage (typically called $V_{forward}$ in data sheets). Given a standard 120 volt AC wall plug receptacle voltage, design a schematic for a circuit that converts the AC voltage to DC Voltage and powers the diode with the rated DC current and voltage to act as a visible power status indicator. At minimal you should use a diode, capacitor, resistor and the LED. Note: do not use and LED as part of any rectifier circuit as they cannot withstand large reverse biases. No need to analyze the circuit $-j^{ugt}$ design it,



13) Pulling all the concepts together for a useful purpose:

(50-points total: DC solution = 15 points, conversion to small signal model = 15 points, AC solution = 15 points and 5 points for accuracy of the graphs and 10 points for the bonus solution of the correct turn on voltage).



For the circuit above, you will be asked to solve for two cases, the second being 10 points Bonus:

- A) The small signal gain of the amplifier and plot the small signal output waveforms Vout 1 and Vout 2 when the input Vinac=1 mV AC and
- B) (Bonus 10 points) The output waveforms when the input Vinac=100 mV AC.

Initially Assume Q1: V_{Base-Emitter} turn on=0.6 V, V_{Base-Collector} turn on=0.4 V, β_{DC}=449.8, Early Voltage=29V, I_s=2.077E-13 Amps

D1: $V_{turn on}=0.6 V$, Is=2.52E-12 amps V_{in} = 1mV amplitude (i.e. 2mV peak to peak) at 1 KiloHertz

Given the above input voltage, Vinac, sketch and accurately label a plot of the output waveforms V_{out1} on the graph paper provided on the next page. Assume the turn on voltages for all forward biased junctions are as described above. You may assume all capacitors are very large values and any inductors are very large values. Additionally consider the circuit to be operated at low frequencies where you can neglect all small signal capacitances of transistors and diodes. Also, neglect all resistances that result from quasi-neutral regions. For full Credit, be sure to check your assumptions on the mode of operation of the transistor and diodes and to clearly label the axes of your plot.

~ /

Small Signal answer Page (NOTE: Vin and Vout can have different y-axis values so label the axis or label the signal peaks)

Part A:



Bonus Part B



Extra work can be done here, but clearly indicate with problem you are solving.

$$\frac{DC}{V_{Her}} = \frac{Q1'}{(R_{5}^{-}+R_{4})} + 9 \left(\frac{R_{4}}{R_{5}^{-}+R_{4}}\right)$$

$$= -0.7122V$$

$$R_{HB} = 12.8K||15K = R_{1} = 0$$

$$= 6.906 D = R_{HB} = 1000 R_{HB}$$

$$= 6.906 D = R_{HB} = 1000 R_{HB}$$

$$= 1000 R_{HB} + 1000 R_{HB}$$

$$= -0.678$$

$$V_{E} = -0.678$$

$$V_{E} = -0.0787 V$$

Extra work can be done here, but clearly indicate with problem you are solving.

$$\begin{array}{l} A_{\mathcal{L}} & Solution \\ R_{6} \\ N_{1} & \xrightarrow{75500} R_{4} H|R_{5} \\ R_{6} \\ N_{7} & \xrightarrow{75700} R_{1} H|R_{5} \\ R_{6} \\ Sign = Sign R_{7} \\ S$$

Extra work can be done here, but clearly indicate with problem you are solving.

